

INDIAN INSTITUTE OF ENGINEERING SCIENCE AND TECHNOLOGY, SHIBPUR

PhD Admissions under "Visvesvaraya PhD scheme"

Sponsored by DeitY, MCIT, Govt. of India

2015-16

Department of Electronics and Information Technology (DeitY), MCIT, Govt. of India has granted 15 scholarships under the scheme titled "Visvesvaraya PhD scheme". Under this scheme presently admissions are open to PhD Degree program on Full Time basis.

Applications ([Application Form](#)) are invited from Indian nationals for admission to PhD Program for the odd semester 2015-16, under the above scheme at this Institute.

The major features of the scheme for the full-time scholars are given below:

- Monthly scholarship of Rs.22, 500/- (likely to be revised to Rs. 31,250/) per month for first two years and Rs.25, 000/- (Likely to be revised to 35,000/-) per month for rest 3 years.
- Rent reimbursement up to 30% of scholarship amount paid per month.
- Annual contingency of Rs.30, 000/- .
- Financial Support (up to Rs. 50,000/-) for attending International conferences

Eligibility criterion: Essential: M.Tech in IT, Electronics, CS, EE and VLSI or equivalent with a minimum of 65% or 7.25 CGPA (60% or 6.25 CGPA for ST/ST candidates) at both the Bachelor's (CS, EE, IT or Electronics) and Master's level.

Desirable: GATE qualification.

Age: 32 years as on 1 July 2015; date may be relaxed by 5 years for SC/ST/OBC/PWD/Woman candidate

Above eligibility is minimum requirement only. Good academic career and higher qualification is preferred and mere eligibility does not ensure a call for admission test and/or Interview. A screening of applications will be made to prepare a short list of candidates who will be called for admission test and/or Interview. Further screening will be done after the admission test to select the candidates for the interview.

The number of scholars in the three department/school with Specializations in the proposed PhD Program is given below:

Sl. No.

Department/School

No. of scholars

Area of Specialization

1

Electronics and Telecommunication

6

Planar circuit and antenna, RFID applications, Meta-materials, Phased array antenna, Electromagnetic b

2

Information Technology

6

Cellular automata, Digital micro-fluidic biochip, Digital Geometry and Image Analysis, Medical Image Processing, VLSI Design and Test, Ad-hoc network, wireless sensor network, Crowd Sourcing, Big Data Analysis, Cloud Computing

3

VLSI

3

VLSI Synthesis and Verification, Algorithms for VLSI CAD, CAD for Digital Micro-Fluidic Biochip, System-on-Chip

- The admission calendar, fee structure, academic regulations will be as per regular PhD admission process of the institute. For details see the PhD regulations in the website: www.iiests.ac.in.

- Applicants seeking admission under this scheme are required to apply on-line by downloading the application form and submitting the duly filled-up admission form (with the details of the draft) and the scanned copy of the M. Tech, DOB, GATE and category certificates, if applicable, (all in PDF form) to the following e-mail address phdvvisya@iiests.ac.in **on or before 4 PM, 30**

- An applicant may make separate applications (with separate draft for each application) to

more than one department/schools if he/she fulfills the eligibility criteria for that department.

- Applicants need to pay Rs. 300/- (Rs. 150/- for the SC/ST candidates) by demand draft/pay order addressed to “REGISTRAR, IEST, Shibpur” and payable at Kolkata.

- The draft (details given by the candidate in the application form) will be treated as admit card and would be collected from the candidates during the test .

Important Dates: LAST DATE OF APPLICATION: 30 June 2015 (up to 4 PM)

Application:

i) Application

starts

ii) Notification of Shortlisted

Candidates for Test

Date & Time of the

TEST and venue

Notification of Shortlisted Candidates after test

Date : Interview

Admission cum enrollment

i) 19 June 2015

ii) 30 June 2015 (website: 6 PM)

3rd July 2015 :

For

a) IT : 10 to 11 AM

b) Electronics: 12 Noon to 1 PM

c) VLSI: 2 PM to 3 PM

Venue: Corresponding department

3 July 2015

4:30 PM

(Website)

4 July 2015

10 AM onwards in the respective department

(Publication of the result on website:

7 July 2015)

12 to 14 July 2015

(verification of the of the original documents followed by payment of fee and enrollment)

Venue: Office of the Dean (Academic) and UCO bank for payment of fees.

- Candidates selected for admission (12 to 14 July, 2015) must come prepared with all original certificates for verification of documents. They are also supposed to submit the photocopies of all their certificates, two passport size photographs and certificate for the blood group.

TEST

- Separate admission test will be held for each department
- 1hr MCQ type with two and a half marks for each correct answer and minus half marks for wrong answer.
- Question will be based on Departmental Core subjects in the UG level for IT and Electronics department; for VLSI it would be a mixture of CS, IT and Electronics.

The candidates are supposed to provide the scanned copy of the following certificates along with the duly filled-in application form (PDF).

1. B.E/B. Tech mark-sheet
2. M.E/M. Tech mark-sheet
3. Score card; if applicable
4. Proof of date of birth.
5. Community certificate (SC/ST) from a competent authority; if applicable

An application without the copies (scanned PDF form) of the required documents is liable to be rejected.

Visit www.iiests.ac.in for announcements on change of dates etc.related to the PhD admission to this scheme.

The application must be mailed to: phdvvisya@iiests.ac.in

with the subject field as “VS Phd application:” followed by the Name of the Department for which the admission is sought (e.g.; VS PhD application : VLSI) on or before 4 PM, 30 June 2015.

For further queries (email only):

Send email to phdvisya@iiests.ac.in with the subject field as “Query for VS”

Dean (academic)

NOTICE

LIST OF THE CANDIDATES SELECTED (Visvesvaraya PhD Fellowship)
FOR THE TEST ON 3RD JULY 2015
WILL BE NOTIFIED TOMORROW AT 11 AM.