

INDIAN INSTITUTE OF ENGINEERING SCIENCE AND TECHNOLOGY, SHIBPUR

PhD Admissions (2nd Phase) under “Visvesvaraya PhD scheme” Sponsored by DeitY, MCIT, Govt. of India 2015-16

Department of Electronics and Information Technology (DeitY), MCIT, Govt. of India has granted 7 scholarships under the scheme titled “Visvesvaraya PhD scheme”. Under this scheme presently admissions are open to PhD Degree program on Full Time basis. Applications ([for the form use this link](#)) are invited from Indian nationals for admission to PhD Program for the even semester 2015-16, under the above scheme at this Institute.

The major features of the scheme for the full-time scholars are given below:

- Monthly scholarship of Rs.22, 500/- (likely to be revised to Rs. 31,250/-) per month for first two years and Rs.25, 000/- (Likely to be revised to 35,000/-) per month for rest 3 years.
- Rent reimbursement up to 30% of scholarship amount paid per month.
- Annual contingency of Rs.30, 000/- .
- Financial Support (up to Rs. 50,000/-) for attending International conferences.

Eligibility criterion: Essential: M.Tech in IT, Electronics, CS, EE and VLSI or equivalent with a minimum of 72.5% or 8.0 CGPA (67.5% or 7.0 CGPA for ST/ST candidates) at both the Bachelor’s (BE/BTech in CS, EE, IT or Electronics) and Master’s level. Candidates with **M Sc** (in CS, Electronics, IT and VLSI with the same percentage of marks or CGPA specified for BE/BTech) degree followed by **M. Tech** degree is also eligible.

Desirable: GATE qualification.

Age: 32 years as on 30 November 2015; date may be relaxed by 5 years for SC/ST/OBC/PWD/Woman candidate

Above eligibility is minimum requirement only. Good academic career and higher qualification is preferred and mere eligibility does not ensure a call for admission test and/or Interview. A screening of applications will be made to prepare a short list of candidates who will be called for admission test and/or Interview. Further screening will be done after the admission test to select the candidates for the interview.

The area of specializations in the four departments/school with Specializations in the proposed PhD Program is with a tentative allocation (may change) of scholars in each is given below:

Sl. No.	Department/School	No. of Scholars	Area of Specialization
1.	Electronics and Telecommunication	1	Wireless power transfer, Small Antennas for wireless/mobile communications, Miniaturized Multiband Wearable Antennas
2.	Information Technology	2	Design and Analysis of Algorithms for application specific chip and system design, 3D IC Design and Testing, Design and Study of Internet of Things
3.	VLSI	2	Logic Synthesis, VLSI Architecture, VLSI Synthesis and Verification, CAD for VLSI, System on Chip Design and Test, 3D IC Design and Test, Digital VLSI Design, Analog and Mixed Signal IC Design & Test
4.	Computer Science and Technology	2	Design and Verification of Cache System/Coherence Controller in Chip Multiprocessors, Cost Effective Fault tolerant Design of QCA Circuits, Information Security in Big Data, Low Density Parity Check (LDPC) Codes for Information Security, Reliable and Energy Efficient Opportunistic Data Collection in Disconnected WSN, Digital Image Processing and Pattern Recognition, Cloud Computing based <i>Adaptive Agricultural Decision Support System</i> Network-on-Chip Design and Test

- The admission calendar, fee structure, academic regulations will be as per regular PhD admission process of the institute. For details see the PhD regulations in the website www.iiests.ac.in.
- Applicants seeking admission under this scheme are required to apply on-line by downloading the application form and submitting the duly filled-up admission form (with the details of the draft) and the scanned copy of the

M. Tech, B. Tech/MSc, DOB, GATE and category certificates, if applicable, (all in PDF form) to the following e-mail address

phdvvisya@iests.ac.in

on or before **4 PM, 30 November 2015**.

- An applicant may make separate applications (with separate draft for each application) to more than one department/school if he/she fulfills the eligibility criteria for that department/school.
- Applicants need to pay Rs. 300/- (Rs. 150/- for the SC/ST candidates) by demand draft/pay order addressed to “REGISTRAR, IEST, Shibpur” and payable at Kolkata.
- The draft (details given by the candidate in the application form) will be treated as admit card and would be collected from the candidates during the test .

Important Dates: **LAST DATE OF APPLICATION: 30 November 2015 (up to 4 PM)**

Application: i) Application starts ii) Notification of Shortlisted Candidates for Test	Date, Time and venue of the TEST	Notification of Shortlisted Candidates after test	Date : Interview	Admission cum enrollment
i) 9 November 2015 ii) 5 December 2015 (website: 6 PM)	17 or 18 December 2015 10:30 to 12:00 Hrs Venue: In the corresponding departments and school (exact date and venue will be notified along with the shortlisted candidates)	19 December 2015 6:00 PM (Website)	20 and 21 December 2015 10 AM onwards in the respective department/school (Publication of the result on website: 24 December 2015)	2 to 4 January 2016 (verification of the original documents followed by payment of fee and enrollment) Venue: Office of the Dean (Academic) and UCO bank for payment of fees.

- Candidates selected for admission (2 to 4 January, 2016) must come prepared with all original certificates for verification of documents. They are also supposed to submit the photocopies of all their certificates, two passport size photographs and certificate for the blood group.
- PhD programme starts from 4 January 2016.

TEST/INTERVIEW

- Same admission test will be held for each department/school
- 1.5 hr MCQ type with two and a half marks for each correct answer and minus half marks for wrong answer.
- Question will be based on 10+2 level mathematics.
- Interview will be primarily on subjects in B Tech (M Sc)/M Tech level and area of specialisation.

Application CHECKLIST

The candidates are supposed to provide the scanned copy of the following certificates along with the duly filled-in application form (PDF).

- B.E/B. Tech/MSc mark-sheet

- ii) M.E/M. Tech mark-sheet
- iii) GATE Score card; if applicable
- iv) Proof of date of birth.
- v) Community certificate (SC/ST) from a competent authority; if applicable

An application without the copies (scanned PDF form) of the required documents is liable to be rejected.

Visit www.iiests.ac.in for announcements on change of dates etc. related to the PhD admission to this scheme.

The application must be mailed to:

phdvvisya@iiests.ac.in

with the subject field as “VS Phd application:” followed by the Name of the Department for which the admission is sought (e.g.; VS PhD application : VLSI) on or before 4 PM, 30 November 2015.

For further queries (email only):

Send email to phdvvisya@iiests.ac.in with the subject field as “Query for VS”

Dean (academic)