

**INDIAN INSTITUTE OF ENGINEERING SCIENCE AND TECHNOLOGY, SHIBPUR
HOWRAH- 711103**

DEPARTMENT OF ELECTRICAL ENGINEERING

TENDER NO. - 172/EE- 3/21(DR) dt- 05.01.2015

**Sealed quotations are invited from bonafide suppliers for of the following item:
FPGA based Temperature Controller Trainer Kit.**

Specifications:-

Xilinx Spartan XC2S50 FPGA with up to 50k system gates and up to 200MHz clock speed

Communicates with computer through standard Webpack

User can develop the circuit/schematic using Xilinx standard Foundation Series software and any development tool for VLSI design

16x2 LCD module

Six Seven Segment Displays

Stepper Motor Controller Interface

8x8 LED Matrix Display

EWSN Status LED's 12 Nos.

ADC/DAC Interface

RS-232 interface

Parallel JTAG Interface

Relay

Opto

Real Time Clock 1380.

At24C04 Serial EEPROM

Eight Data Switches

4x4 Keyboard

Power Indicator LED

On board facility for variable clock generation

Temperature Controller Setup

Hot Plate

Fan

LM35 Temperature Sensor

Last date of submission of bid :- 12-01-2015 up to 3 P.M.