

School of VLSI Technology  
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**VLSI Web Tender Advt. No. 01/IT/UPS/2016-17**  
**Date: 30.09.2016**

**Notice Inviting Quotations**

Complete sealed quotations are invited for the supply of items/works listed below as per specification mentioned. The quotation should include all kinds of taxes and delivery charges of the items to the School of VLSI Technology, IEST, Shibpur. Bidders are to submit Technical Bid and Price Bids separately in two sealed envelopes. The contents of the envelope (Technical Bid / Price Bid) should be mentioned on its top. All the sealed envelopes should be placed in a common sealed envelope, superscribed with the Ref. Advertisement No. and date along with the bidders name and address. The sealed quotations are to be submitted in the tender box in the office of the School of VLSI Technology.

Last date of submission of sealed quotation is 10 days the date of publication and tenders will be opened on the next working day 12 noon.

***ITEMS:***

<b>Sl. No.</b>	<b>Item Details</b>	<b>Quantity</b>
1.	Replacement of old battery with new battery for existing online UPS with the following specifications  12V / 18AH SMF VRLA Battery (Quanta/Exide)  -Separately mention the buy-back price of the old battery	30 Nos.

(Hafizur Rahaman)  
Head of the Department