INDIAN INSTITUTE OF ENGINEERING SCIENCE AND TECHNOLOGY, SHIBPUR

Admission to Full-time PhD programme under"Visvesvaraya PhD scheme
(Sponsored by DeitY, MCIT, Govt. of India)
2016-17

Department of Electronics and Information Technology (**DeitY**), **MCIT**, **Govt. of India** has granted a total of 22 scholarships under the scheme entitled "Visvesvaraiya PhD scheme". Under this scheme, at present, there are 7 vacant positions for full-time PhD scholars. Applications are invited from Indian nationals for admission to PhD Program for the odd semester 2016-17 to fill-in the 7 vacant positions, under the above scheme at this Institute.

The major features of the scheme for the full-time scholars are given below:
- Monthly scholarship of Rs.31, 250/- per month for first 2 years and Rs. 35,000/-) per month for rest 3 years.
- Rent reimbursement up to 30% of scholarship amount paid per month.
- Annual contingency of Rs.30, 000/- .
- Financial Support (up to Rs. 50,000/-) for attending International conferences.

Eligibility criterion: Essential: M.Tech (candidates appearing in the examination may also apply) in IT, Electronics, CS, EE and VLSI or equivalent with a minimum of 72.5% or 8.0 CGPA (67.5% or 7.0 CGPA for ST/ST candidates) at both the Bachelor's (BE/BTech in CS, EE, IT or Electronics) and Master's level. Candidates with M Sc (in CS, Electronics, IT and VLSI with the same percentage of marks or CGPA specified for BE/BTech) degree followed by M. Tech degree are also eligible.
Desirable: GATE qualification.
Age: 32 years as on 1 July 2016; date may be relaxed by 5 years for SC/ST/OBC/PWD/Woman candidate

The above eligibility condition is a minimum requirement only. Good academic career and higher qualification is preferred and mere eligibility does not ensure a call for admission test and/or Interview. A screening of applications will be made to prepare a short list of candidates who will be called for admission test and/or Interview. Further screening will be done after the admission test to select the candidates for the interview.

The area of specializations in the four departments/school with Specializations in the proposed PhD Program is with a tentative allocation (may change) of scholars in each is given below:

Sl. No.	Department/School	No. of Scholars	Area of Specialization
1.	Electronics and Telecommunication	1	Microelectronics/ Electrical Biosensor, Functionalized Graphene derivative based hybrid gas sensor devices, Interconnect device technology and high frequency circuit design
2.	Information Technology	2	Design and Analysis of Algorithms for application specific chip and system design, 3D IC Design and Testing, Design and Study of Internet of Things
3.	VLSI	2	Logic Synthesis, VLSI Architecture, VLSI Synthesis and Verification, CAD for VLSI, System on Chip Design and Test, 3D IC Design and Test, Digital VLSI Design, Analog and Mixed Signal IC Design & Test
4.	Computer Science and Technology	2	Design and Verification of Cache System/Coherence Controller in Chip Multiprocessors, Cost Effective Fault tolerant Design of QCA Circuits, Information Security in Big Data, Low Density Parity Check (LDPC) Codes for Information Security, Reliable and Energy Efficient Opportunistic Data Collection in Disconnected WSN, Digital Image Processing and Pattern Recognition, Cloud Computing based *Adaptive Agricultural Decision Support System* Network-on-Chip Design and Test

- For details see the PhD regulations (likely to changed soon) in the website www.iiests.ac.in.

- Applicants seeking admission under this scheme are required to apply on-line duly filled-up electronic admission form (with the details of the draft).

- An applicant may make separate applications (with separate draft for each application) to more than one department/school if he/she fulfills the eligibility criteria for that department/school.

- Applicants need to pay Rs. 300/- (Rs. 150/- for the SC/ST/PwD candidates) by demand draft/pay order addressed to "REGISTRAR, IIEST, Shibpur" and payable at Kolkata.

- A copy of the submitted application along with the draft (details given by the candidate in the application form) together will be treated as admit card. The draft(s) would be collected from the candidates during the test.

- For candidates appearing in M. Tech. and M. Sc. examinations are required to submit (within September 15, 2016) their marks of the final examination and that would comply the minimum requirement mentioned earlier as the minimum eligibility failing which their candidature will be cancelled and no scholarship will be given to them.

Important Dates: LAST DATE OF APPLICATION: 8 June 2016

Application/Notification	Date, Time and venue of the TEST	Notification of Shortlisted Candidates after test	Date : Interview	Admission cum enrollment
Application starts : 19 May 2016 Notification of Shortlisted Candidates for Test 13 June 2016 (website: 6 PM)	**23 June 2016** **10:30 to 12:00 Hrs** Venue: In the corresponding departments and school (exact date and venue will be notified along with the shortlisted candidates)	25 June 2016 6:00 PM (Website)	27 June 2016 10 AM onwards in the respective department/school (Publication of the result on website: 27 June 2016)	30 June and 1 July 2016 (verification of the of the original documents followed by payment of fee and enrollment) Venue: Office of the Dean (Academic) and UCO bank for payment of fees.

- Candidates selected for admission (30 June and 1 July, 2016) must come prepared with all original certificates for verification of documents. They are also supposed to submit the photocopies of all their certificates, two passport size photographs and certificate for the blood group.
- VVS PhD programme starts from 1 July 2016.

TEST/INTERVIEW

- Same admission test will be held for each department/school
- 1.5 hr MCQ type with two and a half marks for each correct answer and minus half marks for wrong answer.
- Question will be based on 10+2 level mathematics.
- Interview will be primarily on subjects in B.Tech (M.Sc)/M .Tech level and area of specialisation.

Interview/Admission CHECKLIST

The candidates are supposed to produce the following documents at the time of Interview and Admission
i)   B.E/B. Tech/MSc* mark-sheet
ii)   M.E*/M. Tech* mark-sheet

iii)   GATE Score card; if applicable

iv)   Proof of date of birth.

v)   Community certificate (SC/ST), PwD certificate from competent authority; if applicable

- **** Candidates appearing in the final examination may submit results up to pre-final semester.***
- **** Scanned copy of the above documents and two stamp sized photographs are to be submitted during admission.***

Dean (academic)