

Indian Institute of Engineering Science and Technology, Shibpur



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Dated 11.12.2017

Indian Institute of Engineering Science and Technology, Shibpur, an Institute of National Importance engaged in teaching, research and development requires temporary faculty for the Department of Mining Engineering and for the School of VLSI Technology, IIEST, Shibpur. The Institute invites application from Indian nationals, possessing excellent academic background, commitment to top quality teaching and proven credentials for carrying out outstanding research and development for the said Center.

For the Department of mining engineering

- Name of the Post** : ASSISTANT PROFESSOR (Temporary Faculty)
- No. of Post** : 02
- Remuneration** : Rs.39,000/- (consolidated)
- Essential Qualification** :
- i) Good academic record with at least 55% marks (or and equivalent grade in a point scale wherever grading system is followed) at the Master's Degree level in Geography from an Indian University or an equivalent degree from an accredited foreign university.
 - ii) Besides fulfilling the above qualifications, the candidate must have cleared the National Eligibility Test (NET) conducted by the UGC, CSIR or similar test accredited by the UGC like SLET/SET.
 - iii) Notwithstanding anything contained in sub-clauses (i) and (ii) of this Clause, candidates, who are, or have been awarded a Ph.D. Degree in accordance with the university Grants Regulations, 2009, shall be exempted from the requirement of the minimum eligibility condition of NET/SLET/SET from recruitment and appointment of Assistant Professor or equivalent positions in Universities/Colleges/Institutions.
 - iv) NET/SLET/SET shall also not be required for such Masters Programmes in disciplines for which NET/SLET/SET is not conducted.
- Desirable Qualification** : PhD in Remote Sensing and GIS application.
- Experience** : 3 years experience in teaching and research in Remote Sensing and GIS after PhD.

For the School of VLSI technology

Name of the Post : ASSISTANT PROFESSOR (Temporary Faculty)

No. of Post : 02

Remuneration : Rs.39,000/- (consolidated)

Essential Qualification : First Class M.Tech. in Electronics/VLSI Design /Equivalent and PhD Degree (Awarded or Submitted) related to (VLSI Design).

Experience : Experience in handling the CAD tools (Synopsys /Cadence /Mentor Graphics and TCD etc.) is preferable.

Reservation : Without any compromise on qualification, experience and competence, reservation as per Government of India rules is applicable.

Tenure : *One semester, extensible upto five semester.*

General Information

- Degrees referred above should have been awarded by a recognized University / Institute.
- Mere eligibility will not vest any right on any candidate for being called for interview. The decision of the Institute in all matters will be final. No correspondence will be entertained from the candidates in connection with the process of selection / interview.
- The Institute reserves the right to call for interview only those candidates shortlisted on the basis of their qualification, experience, research and publication records and departmental requirements etc.
- The Institute reserves the right to fill or not to fill any or all the posts advertised.
- Persons employed in Government Organizations / Quasi Government Organizations should submit their application through proper channel.
- On special request by a candidate selected for interview but staying abroad, an interview through Skype may be arranged.
- Canvassing in any manner may entail disqualification of the candidature.
- Any dispute with regard to the selection / recruitment process will be subject to Courts / Tribunals having jurisdiction over Kolkata.

Candidates possessing requisite qualification & experience are required to apply in prescribed application form and Bio-data sheet (downloaded from the website www.iiests.ac.in) and to submit to the Receiving Section of the Institute either in person or by post. The application, completed in all respect along with eight (8) sets of photocopies of it and Biodata sheet must reach to the “**Dean, Faculty Affairs, IEST, Shibpur, WB -711103, India**”. **The Last date of submission of application is 20.12.2017.** A scanned copy of the application form should also be sent to dean.fa@iiests.ac.in. Only one set of self-attested copy of mark sheet(s), certificate(s) and testimonial(s) etc. is to be attached with the original.

Dean, Administrative Affairs