

OFFICE OF THE CONTROLLER OF EXAMINATIONS
INDIAN INSTITUTE OF ENGINEERING SCIENCE AND TECHNOLOGY, SHIBPUR

Programme of **M.Tech** Odd semester Final Examinations to be held in November-December 2017

Date of Commencement: 27. 11. 2017

Time: 2.00 PM - 5.00PM

DATE	VLSI DESIGN	MATERIALS ENGG.	MECHATRONICS
27/11/2017	Advanced System Architecture (VLSI-904/1)	Introduction to Materials (MT- 901)	Applied Engg Mathematics (MEC 101)
28/11/2017	Digital Signal Processing (VLSI-905/2)		Advanced Control System (MEC 104)
29/11/2017	VLSI Circuit & System (VLSI-902)	Advanced Processing of Metals (MT-908) Surface Engineering (MT 906) Polymeric Materials (MT 1002) Composite Materials (MT 1004)	Mechatronics System Design (MEC 102)
30/11/2017	VLSI Testing (VLSI-903) SOC and Memory Design and Test (VLSI-1150/2)	General Methodology of Modeling and Optimization (MT 904)	Smart Material, Sensors & Actuators (MEC 103)
01/12/2017	Semiconductor Physics & Devices (VLSI-901)	Metallic Materials (MT-903)	Bridge Course Elective I for Mechanical Mechatronics (MEC 105/1) Bridge Course Elective I for EE/Elect (MEC 105/2)
04/12/2017		Material Characterization. (MT-902)	

Dr. N. K. Bhattacharyya
Controller of Exams.(Acting)IIEST