

OFFICE OF THE DEAN ACADEMIC
INDIAN INSTITUTE OF ENGINEERING SCIENCE AND TECHNOLOGY, SHIBPUR

PROGRAMME

M.Tech(1st & 3rd Semester) Final Examinations to be held in November 2019

Date of Commencement: 19. 11. 2019

Time: 2.00 PM - 5.00PM

DATE	VLSI DESIGN	MATERIALS SCIENCE & ENGINEERING	MECHATRONICS
19/11/2019	Analog VLSI Circuits (VL5102)	Introduction to Materials (MT5101)	Mechatronics System Design (MC5101)
20/11/2019	Semiconductor Devices & Modeling(VL5101) SOC and Memory Design and Test (VLSI-1150/2)[for 3 rd Semester]	Advanced Materials Characterization.(MT5102)	Sensors & Actuators (MC 5102)
22/11/2019	Advanced Systems Architecture (VL5104/1) [Time 10.00 AM – 1.00 PM]	Low Dimensional Materials(MT5104)	Fundamentals of Electronics Engineering(MC5104) Fundamentals of Mechanical Engineering(MC5105)
26/11/2019	Logic synthesis and Verification VL 5105/3 [Time 10.00 AM – 1.00 PM]	Crystallography and X-ray diffraction (MT 5105) [Time 10.00 AM – 1.00 PM]	
27/11/2019	Digital VLSI Circuits (VL5103)	Metallic Materials (MT5103)	Advanced Control System (MC 5103)

Dr. N. K. Bhattacharyya
Deputy Registrar (Academic)