

**Office of the Dean Research and Consultancy
Indian Institute of Engineering Science & Technology (IEST), Shibpur
Howrah-711 103**

**Project on: "Design and Implementation of a Comparison-Free Scalable High-Throughput
Energy-Efficient Hardware Sorting Engine for FPGA-Based Embedded System
Applications"**

**[Sponsoring Authority: Department of Science & Technology and Bio-technology,
Govt. of West Bengal]**

**Department of Computer Science and Technology,
Indian Institute of Engineering Science & Technology (IEST), Shibpur
Howrah-711 103**

**Ref.: Advt. No. CS 1484, published in the "Statesman (All Edition)" & "Dainik Statesman",
dated 11.04.2019**

[Institute Project Code: DRC/DST-WB(STB)/CST/SG/024/18-19]

A walk in-interview will be held on **14th May 2019** at **11.00 AM**, for the following temporary posts in the **Department of Computer Science and Technology, Indian Institute of Engineering Science & Technology (IEST), Shibpur, Howrah-711 103.**

Position: Junior Research Fellow (JRF)

No of post – One (JRF may be extended to SRF based on performance in the project)

Essential Qualification: First class B.E / B.Tech. or M.E. / M.Tech. in Computer Science and Engineering / Information Technology / Electronics Engineering with valid GATE score.

Desirable Qualification: Knowledge in Computer Organization and Architecture, Hardware designs using HDL and / or experience in FPGA based tools.

Fellowship: Rs. 18,700/- per month for 1st year and 2nd year,

Rs. 21,000/- per month for 3rd year (subject to the performance in the project)

Duration: Three years. (Initially for 1 year but may be extended for 2 more years subject to condition of the performance of the candidate.)

Note: The candidate may pursue Ph.D. programme on this topic (subject to satisfactory performance).

Age Limit: Maximum 28 years, (Upper age limit is relaxed up to 5 years for SC/ST/OBC/Woman and Physically handicapped candidates).

How to Apply: Competent Candidate should bring two hard copies of resume which include (1) Name, (2) Parent's Name, (3) Date of Birth, (4) Complete Postal Address, (5) E-mail, (6) Contact Telephone No., (7) Mobile Ph. No., (8) Academic Qualifications from 12th standard, (9) Work experience, (10) Publications (if any) and a recent passport-size photograph to submit and one set of self-attested copies of mark sheets and certificates.

Candidates are also advised to **send the resume in advance** by e-mail to Dr. Surajeet Ghosh (E-mail id: surajeet@cs.iiests.ac.in / ghoshsurajeet@yahoo.com).

Venue of the interview:

Meeting Room,
Department of Computer Science and Technology, IEST, Shibpur.

Terms and Conditions:

1. Selected candidates will not be entitled to claim for regular appointments / absorption in this institute.
2. No TA/DA will be paid for attending the interview.
3. Candidates appearing in the interview should bring all the original mark-sheets, certificates etc.
4. Applicants reporting after 12 Noon on 14th May 2019 may not ordinarily be entertained.
5. Canvassing in any form will disqualify the candidature.

For further details please contact:

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Principal Investigator

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Dean (R & C)

(W. Code DRC-031/18-19)