

**Office of the Dean Research and Consultancy
Indian Institute of Engineering Science & Technology (IEST), Shibpur
Howrah-711 103**

Project on: "Design and Implementation of Pipeline Architecture for DNA Sequence Alignment"

[Sponsoring Authority: DBT-Ministry of Science and Technology, Govt. of India]

**Department of Computer Science and Technology,
Indian Institute of Engineering Science & Technology (IEST), Shibpur
Howrah-711 103**

Ref.: Advt. No. CS 1263, published in the "Statesman (All Edition)" & "Dainik Statesman", dated 20.09.2018

[Institute Project Code: DRC/DBT/CST/SG/004/17-18]

A walk in-interview will be held on 1st October 2018 at 11.30 AM, for the following temporary position in the department of Computer Science and Technology, Indian Institute Engineering and Science Technology, Shibpur, Howrah.

**Position: Junior Research Fellow (JRF)
No of post – One**

Essential Qualification: First class B.E / B.Tech. in Computer Science and Engineering / Information Technology / Electronics Engineering with valid GATE score or M.E. / M.Tech. in Computer Science and Engineering / Information Technology / Electronics Engineering.

Desirable Qualification: Knowledge in Computer Organization and Architecture and Hardware designs using HDL and / or experience in FPGA based tools.

Fellowship: Rs. 25000+30% HRA per month

Duration: 1 Year 7 Months (Approx.). (Initially for 1 year but may be extended subject to condition of the performance of the candidate.)

How to Apply: Competent Candidate should bring two hard copies of resume which include (1) Name, (2) Parent's Name, (3) Date of Birth, (4) Complete Postal Address, (5) E-mail, (6) Contact Telephone No., (7) Mobile Ph. No., (8) Academic Qualifications from 12th standard, (9) Work experience, (10) Publications (if any) and a recent passport-size photograph to submit and one set of self-attested copies of mark sheets and certificates.

Candidates are also advised to send the resume in **advance** by e-mail to Dr. Surajeet Ghosh (E-mail id: surajeet@cs.iests.ac.in / ghoshsurajeet@yahoo.com).

Venue of the interview:

Meeting Room,
Department of Computer Science and Technology,
IEST, Shibpur.

Terms and Conditions:

1. Selected candidates will not be entitled to claim for regular appointments / absorption in this institute.
2. No TA/DA will be paid for attending the interview.
3. Candidates appearing in the interview should bring all the original mark-sheets, certificates etc.
4. Applicants reporting after 12 Noon on 1st October 2018 may not ordinarily be entertained.
5. Canvassing in any form will disqualify the candidature.

For further details please contact:

Dr. Surajeet Ghosh,
Principal Investigator

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Dean (R & C)

(W. Code DRC-015/18-19)