Office of the Dean Research and Development
Indian Institute of Engineering Science & Technology (IIEST), Shibpur
Howrah-711 103

Project on: "Special Manpower Development Programme for Chips to System Design (SMDP-C2SD)"

[Sponsoring Authority: DEITY, Govt. of India]

School of VLSI Technology,
Indian Institute of Engineering Science & Technology (IIEST), Shibpur
Howrah-711 103

Ref.: Advt. No. VL 1256, published in the "Statesman (All Edition)", dated 23.05.2017

[Institute Project Code: DRC/DEITY-MCIT/VLSI/HR/006/15-16]

Interested candidates are requested to appear for a walk in-interview on **8th June 2017** at **02.00 PM**, for the following temporary posts in the School of VLSI Technology, Indian Institute of Engineering Science & Technology (IIEST), Shibpur Howrah.

Research Assistant: No of post - One

Essential Qualification: M.E / M.Tech in Electronics Engineering / Equivalent.

Experience: Teaching and Industrial experience in the field of VLSI CAD tools is desirable.

Salary: Rs. 20,000/- (consolidated) for Four years (Yearly Increment 10%)

Duration: The appointments are purely in temporary basis. The tenure of the appointments will be initially for one year, which may be renewed further on the basis of successful performance of the candidate. Competent candidates should bring two hard copies of bio-data to submit and original mark sheet and certificate.

Venue of the interview: Office of the Dean (Research and Development), 6th Floor, New Technology Building, IIEST, Shibpur.

NB: Soft copy of the bio-data may be sent through e-mail in advance to: Prof. Hafizur Rahaman,
E-mail id: rahaman.h@gmail.com

Dean (R & D)

(W. Code DRC-007/17-18)