DEPARTMENT OF ELECTRICAL ENGINEERING
INDIAN INSTITUTE OF ENGINEERING SCIENCE AND TECHNOLOGY, SHIBPUR
AN INSTITUTE OF NATIONAL IMPORTANCE, UNDER MHRD, GOVT. OF INDIA
(Formerly BENGAL ENGINEERING AND SCIENCE UNIVERSITY, SHIBPUR)
P.O. BOTANIC GARDEN, HOWRAH-711 103, WB, INDIA

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Advt.No.WEB/IIEST/EE/17-18/38

From : The Head of the Department,
Electrical Engineering,
IIEST, Shibpur, Howrah-711 103

To : Enlisted vendors of the institute and other interested parties.

Dear Sir(s),

Sealed quotations are invited for supply of the following item(s) within **21 days** from the date of publication of this advertisement in the website. The quotation should include the taxes as per rule, delivery charges, entry tax if any, etc. to Indian Institute of Engineering Science and Technology, Shibpur and should mention a firm delivery period. Preferences will be given to the supplier who can supply ex-stock. The minimum two years warranty must be provided.

Yours faithfully,

(Ashoke Sutradhar)
Head of the Department,
Electrical Engineering
IIEST, Shibpur, Howrah – 711 103

List of Items:

1. FPGA BASED DEVELOPMENT BOARD (Two Numbers)

Specification:

The board must be based on Field Programmable Gates Array (FPGA) processor chip (Make Altera or Xilinx) integrated properly with requisite power supply, Analog-to-Digital Converter (ADC's minimum 12bit), Digital-to-analog Converters (DAC's minimum 12 bit), buffers, driver, interface card etc. The total solution has to be confined within preferably one PCB or in two PCB's in the worst case. Design should be compact, modular and such that EMI problems are absent. 8 no.s of ADC channels (with 8 more spares), 4 numbers of DAC channels, 60 Input/Output (I/O) lines minimum, 12k logic elements minimum Serial programming option, JTAG equivalent byteblaster cable (USB computer port compatible) should accompany the kit.

In-built 64 kB serial EEPROM memory Minimum 4 MB flash memory (active serial configuration device).

User/Customer will connect the board to a 230V, 50 Hz single phase AC mains and all internal power supply, as applicable, should be derived as per internal design of vendor. The entire system should be housed inside a proper cubicle. ADC channel inputs, DAC channel outputs and all user-usable digital I/O pins should be externally terminated to suitable screw-type connectors for user's access.

Optional: RS232 interface/USB interface/LCD interface/CAN interface features

Warranty: Two Years

Telephone : (033) 2668-4561-63(3 lines) or (033) 2668 0521 to 0525 (5 lines) Extn.-225, Fax: (033) 2668-2916; E-mail: hod@ee.iiests.ac.in