Department of Physics
Indian Institute of Engineering Science & Technology, Shibpur
Howrah – 711103

No. 02/IIEST/Phy/SS/CPDA/Lab.Equip/2016-17   Dated: 13.02.2017

Advt.No.Web/PHY/IIEST/16-17/88

Notice Inviting Quotation

Sealed quotations are invited for supply of the following items/equipment or to carry out works listed below as per mentioned specifications. The relevant bidding document can be downloaded from the website. The document can be also obtained from the Department of Physics (contact: Dr. S Sarkar) between 12.00 a.m. and 4.00 p.m. on all working days The quotation should include all kinds of taxes/duties and delivery charges of the items to the Office of the Department of Physics, IIEST, Shibpur, Howrah-03. Last date of submission of sealed quotation is **7 working days from the date of publication** in the Website of the Institute and tenders will be opened on the next working day at 12 noon.

Dr. Sukhendusekhar Sarkar
Physics
IIEST,Shibpur, Howrah – 03.

List of Items:

In-built FPGA for Digital Pulse Processing

Should have following Features:

- Should be suitable for Real Time Digital Pulse Processing
- Should be suitable for Spectroscopic acquisition and Time Information
- Should be suitable for Waveform, internal signal analysis

Specifications

Communication interface	**Optical Link** Up to 80 MB/s transfer rate Daisy chain capability: it is possible to connect up to 8 or 32 ADC modules to a single Optical Link Controller (A2818 or A3818 respectively)	**USB** USB 2.0 compliant Up to 30 MB/s transfer rate
Front Panel Digital I/O	**CLK-IN (AMP Modu II)** AC coupled differential Input Clock: LVDS, ECL, PECL, LVPECL, CML (single ended NIM/TTL available by orderable cable); Jitter<100ppm requested; can be used as external clock reference for single board or to synchronize the clocks of multiple boards, provided through a Fan In **GPO (LEMO)** General Purpose Output: NIM/ TTL, Zin = 50 Ω Can be used to propagate the global trigger in multi-board synchronization (in combination with TRG-IN), as output register or Run ON/OFF status	**TRG-IN (LEMO)** External Trigger Input: NIM/TTL, Zin = 50 Ω Can be used to force the event acquisition from all the channels of the board, to gate/veto the individual channel triggers, or to propagate the common trigger in multi-board synchronization (in combination with GPO) **GPI (LEMO)** General Purpose Input: NIM/TTL, Zin = 50 Ω Can be used as SYNC/START in multi-board synchronization or Run ON/OFF Control